



# Highly Crystalline and Stoichiometric Growth of CdTe by Cost-Effective Hydrothermal Technique

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## Abstract

CdTe powder samples have been synthesized using a hydrothermal approach, employing cadmium acetate and tellurium dioxides as sources of Cd and Te, respectively. NaBH<sub>4</sub> was utilized as the reducing agent, and double-distilled water was used as the solvent in the synthesis process. The effect of annealing on the synthesized samples was investigated. The samples have been characterized by x-ray powder diffraction, Raman spectroscopy, UV-Vis-NIR spectroscopy, scanning electron microscopy, energy dispersive spectroscopy, and x-ray photoelectron spectroscopy. The electrical properties (current–voltage and capacitance–voltage) of the as-prepared and annealed CdTe pellets were investigated. These findings indicate that annealing can lead to improvements in crystallinity, crystallite size, and electrical conductance. This paper presents a simple, cost-effective, and versatile method for producing significant amounts of CdTe nanostructure powders with properties suitable for use in the fabrication of CdS/CdTe solar cells.

**Keywords** Hydrothermal technique · CdTe · thin film · structural properties · stoichiometric growth

## Introduction

Cadmium telluride (CdTe) is an emerging thin-film solar cell (TFSC) technology owing to its optimum optical energy band gap of 1.45 eV and high absorption coefficient ( $> 10^5 \text{ cm}^{-1}$ ) in the visible region.<sup>1,2</sup> The highest efficiency for CdS/CdTe thin film solar cells of ~ 22.1% has been reported by First Solar using the close space sublimation growth technique<sup>3</sup>. However, the reported efficiency is

reasonably lower than the theoretical limit of 30% set by Schockley–Queisser (S-Q) for single-heterojunction solar cells.<sup>4</sup> Several issues need to be resolved to achieve efficiency close to the S-Q limit, such as back low resistive contact to CdTe, highly crystalline less defective CdTe materials, the use of appropriate oxidation and reducing reagents to achieve Te-rich surfaces, etc. Surface defects, such as stacking faults, twins, and grain boundaries, can trap charge carriers, reduce carrier mobility, increase recombination, and limit the carrier lifetime.<sup>5,6</sup> These parameters can lead to a reduction in the performance of TFSC devices. Post-deposition annealing of CdTe surfaces with CdCl<sub>2</sub> treatment is widely used to reduce the defect density and TO improve cell performance.<sup>7,8</sup> Doping the CdTe with an appropriate dopant results in an improvement in its electrical properties, which reduces the recombination of charge carriers and improves its efficiency<sup>9–12</sup>. The performance of the devices is also strongly dependent on the surface morphology, chemical composition, and electrical properties of the CdTe materials.<sup>13–15</sup>

Several methods have been developed to synthesize high-quality CdTe samples, including thermal evaporation,<sup>16,17</sup> close space sublimation,<sup>18</sup> electron beam evaporation,<sup>19</sup> electrodeposition,<sup>20</sup> pulsed laser deposition,<sup>21</sup>

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spray pyrolysis<sup>22</sup>, magnetron sputtering,<sup>23</sup> and colloidal techniques.<sup>24,25</sup> High-efficiency CdTe cells are generally prepared using vacuum-based techniques that produce highly compact, large-grain layers. However, the requirements for high-purity chemicals and sophisticated instrumentation increase capital investment. Moreover, these methods may not be easily transferable from laboratory to industrial-scale production. In addition, many of the aforementioned approaches may not always result in pure stoichiometric CdTe products containing excess Te as an impurity. These impurities can result in non-uniformity and defects in the CdTe structure, affecting its electronic and optical properties.<sup>26,27</sup> Among the various synthesis methods, the hydrothermal technique is commonly employed to prepare metal, semiconductor, and oxide samples owing to its simplicity, low cost, high reproducibility, relatively low temperature, and environmentally friendly process. The hydrothermal environment throws off impurities present in the reaction system.<sup>28–30</sup> Two approaches are commonly used to prepare colloidal CdTe nanostructures. One is the organometallic synthesis method,<sup>31,32</sup> which involves the high-temperature thermolysis of the precursors. However, a major limitation of this approach is the potential toxicity associated with the use of organometallic precursors in the synthesis process. The second approach is to grow CdTe colloidal nanostructures in an aqueous medium, which is simple, inexpensive, and less toxic.<sup>33</sup> Furthermore, this approach allows for precise control of the particle size and shape, morphology, stoichiometry, and crystallinity of the synthesized nanostructures by altering various reaction parameters, including temperature, reaction time, pH, pressure, reducing reagent, and precursor concentration.<sup>34,35</sup> Although sodium hydrogen tellurite (NaHTe) is a commonly used precursor in the aqueous synthesis of CdTe colloids, it is unstable and expensive.<sup>25</sup> An alternative, simpler approach involves the use of tellurium dioxide as a source of the Te precursor in the synthesis process. The synthesis of high-quality CdTe materials is essential to achieve high-performance CdTe TFSC devices.

We report a simple hydrothermal approach for the synthesis of CdTe nanostructures using cadmium acetate, tellurium dioxide, and NaBH<sub>4</sub> in an aqueous solution. We investigated the impact of post-deposition heat treatment on the physical and electrical properties of the CdTe samples. Approximately 250 mg of the CdTe powder sample was pressed into a round pellet at room temperature under a pressure of 5 tons, without an organic binder. Using the two-probe method, we performed current density–voltage and capacitance–voltage measurements for both as-prepared and annealed CdTe pellets (annealed at 400°C for 15 min). Our findings demonstrate that the annealing of CdTe samples results in an improvement in both the

physical and electrical properties suitable for solar cell applications.

## Experimental

The Te precursor solution, consisting of 0.04 mol TeO<sub>2</sub>, was prepared in 50 ml of double-distilled water (DDW) and was constantly stirred in a beaker. Then, 1 M NaOH solution was added dropwise to the milky-white TeO<sub>2</sub> suspension until it became a clear and transparent solution of sodium tellurite (solution A). A Cd precursor solution was prepared in another beaker using an aqueous solution of 0.04 mol cadmium acetate under constant stirring (solution B). Solutions A and B were heated to 60°C with continuous vigorous stirring. Subsequently, 80 mg NaBH<sub>4</sub> was added to the mixture. The obtained product was transferred to a sealed Teflon-lined stainless-steel autoclave and heated at 180°C in a muffle furnace for 24 h. After natural cooling, the precipitate was collected, and centrifuged, washed with DDW and ethanol at least five times, and the final suspension was dried in a hot-air oven. A dark gray powder was collected and thoroughly characterized.

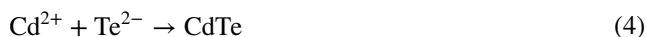
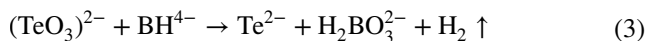
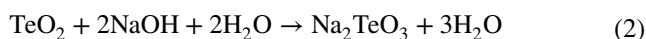
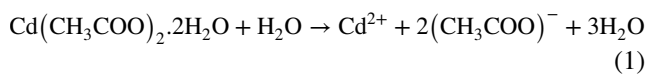
## Sample Characterization

The optical properties of the obtained products were studied using a JASCO V-770 UV-Vis-NIR spectrophotometer. The structural properties were studied using x-ray diffraction (XRD) with Cu K $\alpha$  radiation ( $\lambda = 0.154$  nm). The microstructural properties were investigated using a micro-Raman spectrophotometer with an excitation wavelength of 785 nm (InViaRenishaw). The surface morphologies of the samples were investigated using scanning electron microscopy (SEM) with an operating voltage of 20 kV (JSM 6360-A; JEOL). The elemental compositions of the samples were determined using energy-dispersive spectroscopy (EDS) attached to the SEM unit. The chemical states and surface compositions of the samples were investigated using XPS (PHI 5000 Versaprobe-II; ULVAC-PHI). Current density–voltage and capacitance–voltage measurements were performed at room temperature using a two-probe method setup using a Biologic potentiostat with a booster facility to measure the current in a picoampere.

## Reaction Mechanism

Solid cadmium acetate dissolved in DDW underwent dissociation into its constituent ions, Cd<sup>2+</sup> and (CH<sub>3</sub>COO)<sup>−</sup>, which were uniformly distributed throughout the DDW. (TeO<sub>3</sub>)<sup>2−</sup> was reduced to Te<sup>2−</sup> ions from Na<sub>2</sub>TeO<sub>3</sub> in the presence of NaBH<sub>4</sub>, which reacted with Cd ions to form

CdTe. The following reactions occurred sequentially to form CdTe powder suspensions:



## Results and Discussion

### X-ray Diffraction

The XRD patterns of the as-prepared and annealed CdTe powder samples are shown in Fig. 1. The reflection peaks observed at approximately  $23.42^\circ$ ,  $38.94^\circ$ ,  $46.06^\circ$ , and  $56.49^\circ$  were indexed to the Bragg diffraction planes (111), (220), (311), and (400), respectively, of the cubic crystal structure of CdTe.<sup>20,36</sup> It is noteworthy that impurity and metallic Te peaks were not observed in the XRD spectra of either the as-prepared or annealed samples, which indicates that the chemical reaction between the Cd and Te ions was completed within the optimized initial chemical contents, as well as the reaction time and temperature. The XRD pattern of the as-prepared powder samples exhibited broader peaks that could be associated with the growth of nano-sized particles, which was confirmed by the optical properties. Upon annealing, the crystallinity of the powder sample increased drastically, as confirmed by the values of the full width at half-maximum (FWHM). This improvement in crystallinity could be due to the increase in thermal energy during the

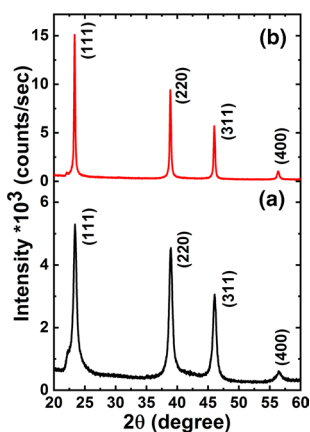
annealing process, which assists in the movement of atoms to the proper lattice positions.<sup>37,38</sup> As a result, the defects are reduced and more orderly crystal structures are formed, which leads to recrystallization of the CdTe lattice.

The slight variation in the measured values of the interplanar distance,  $d$ , compared with its standard counterpart could be due to the strain present in the sample. The strain present on the sample and average crystallite size was calculated using the Williamson–Hall (W-H) equation<sup>39</sup>:

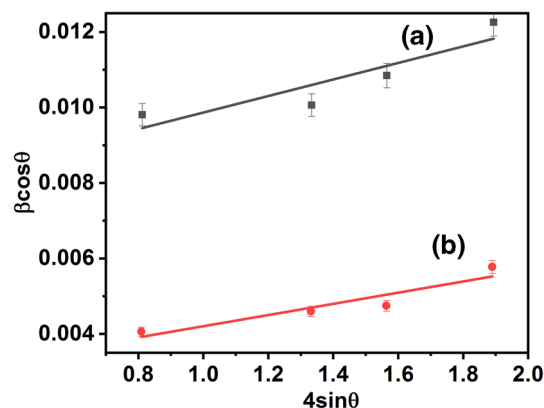
$$\beta \cos \theta = \frac{K\lambda}{D} + 4\varepsilon \sin \theta \quad (5)$$

where  $\beta$  is the FWHM,  $\lambda$  is the x-ray wavelength ( $\lambda = 1.540 \text{ \AA}$ ),  $D$  is the average crystallite size,  $\varepsilon$  is the strain, and  $K$  is a shape-dependent constant.

The W-H plot of  $(\beta \cos \theta)$  versus  $(4 \sin \theta)$  of the observed diffraction peaks is shown in Fig. 2. The y-intercept and slope of the fitted line give values of the average crystallite size and internal microstrain, respectively. The values of ' $D$ ' and ' $\varepsilon$ ' were calculated to be 18 nm and 51 nm and 0.0022 and 0.0015 for the as-prepared and annealed samples, respectively, and the positive values of strain obtained from the slope represent the presence of tensile strain in the crystal structure.<sup>40</sup> The average crystallite size was observed to be increased and the internal microstrain decreased for the annealed CdTe sample. An increase in crystallite size following the annealing process can be ascribed to enhanced surface mobility of small crystals coalescing to form large crystals.<sup>41</sup> The lattice strain arises due to the displacement of atoms from their reference lattice positions in a crystal lattice. The decrease in internal microstrain for the annealed sample revealed a reduction in the concentration of lattice imperfections.<sup>42</sup> These results are consistent with those reported by Khan et al. and Islam et al., where the authors reported that strains were released in annealed CdTe samples.<sup>43,44</sup> According to the literature, it has been documented



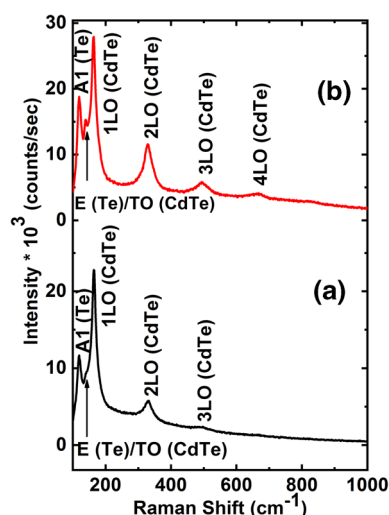
**Fig. 1** XRD patterns of (a) as-prepared and (b) annealed CdTe nanostructure powder samples.



**Fig. 2** W-H plot of (a) as-prepared and (b) annealed CdTe nanostructure samples.

**Table I** Structural parameters of CdTe nanostructure samples

Sample	$2\theta$ (degree)		Miller indices	FWHM (Rad.)	$d$ (Å°)		Phase assignment
	Standard	Observed			Standard	Observed	
As-prepared	23.75	23.42	(111)	0.0100	3.746	3.799	CdTe
	39.31	38.94	(220)	0.0106	2.290	2.313	CdTe
	46.43	46.06	(311)	0.0118	1.954	1.970	CdTe
	56.81	56.52	(400)	0.0139	1.621	1.628	CdTe
Annealed	23.75	23.35	(111)	0.0041	3.746	3.810	CdTe
	39.31	38.88	(220)	0.0049	2.290	2.317	CdTe
	46.43	46.02	(311)	0.0052	1.954	1.972	CdTe
	56.81	56.38	(400)	0.0066	1.621	1.632	CdTe

**Fig. 3** Raman spectra of (a) as-prepared and (b) annealed CdTe nanostructure powder samples.

that the reductions in microstrain, dislocation density, and structural defects of annealed CdTe films result in an increase in crystallite size.<sup>44–46</sup> The values of the structural parameters are set out in Table I.

### Raman Analysis

The microcrystalline behavior of the CdTe powder samples was investigated using Raman spectroscopy. The Raman spectra of the as-prepared and annealed samples at 785-nm excitation wavelength are depicted in Fig. 3. The peak observed at approximately 120 cm<sup>−1</sup> was identified as the A1 phonon mode of Te. A small peak attributed to approximately 140 cm<sup>−1</sup> was assigned to the transverse optical (TO) phonon mode of CdTe and the E phonon mode of Te. It has been reported that Te vibrational modes are typically seen in CdTe powder samples or films.<sup>21,47</sup> It has been observed that the TO phonon mode of CdTe becomes more distinguishable after annealing the sample, owing to enhancement in its crystallinity. The intense peak observed at about 164 cm<sup>−1</sup>

corresponds to the longitudinal optical (LO) phonon mode of CdTe and its multiple overtones 2LO, 3LO, and 4LO at about 328 cm<sup>−1</sup>, 493 cm<sup>−1</sup>, and 667 cm<sup>−1</sup>, respectively. Researchers have reported that, when the energy of incoming or outgoing photons coincides with the band gap of the material, resonance Raman scattering occurs and, as a result, intense Raman spectra with several overtones arise.<sup>48,49</sup> The intensity ratio of 2-LO to 1-LO was calculated to determine the crystalline quality of the CdTe sample, and was found to be increased from 0.25 (as-prepared) to 0.41 (annealed), which revealed an improvement in the crystallinity of the CdTe samples. The Raman results were consistent with those of the XRD analysis.

### Optical Properties

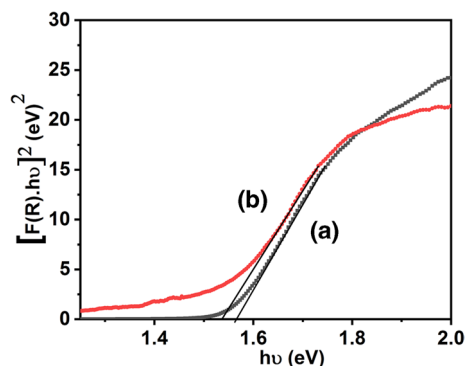
The optical properties of the CdTe samples were studied using UV-Vis-NIR diffuse reflectance spectroscopy. The Kubelka–Munk (K-M) Tauc relationship was used to estimate the optical energy band gap<sup>50</sup>:

$$[F(R_{\infty}) \cdot hv]^2 = A(hv - E_g) \quad (6)$$

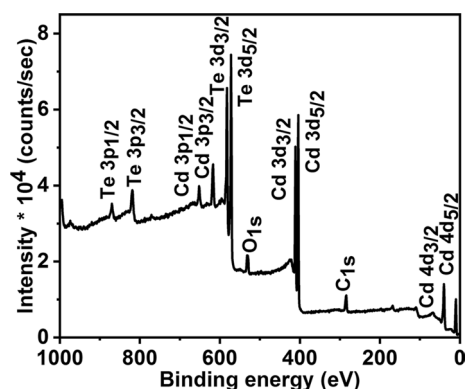
where  $h$  is Planck's constant,  $\nu$  is the frequency,  $E_g$  is the energy band gap,  $A$  is the proportionality constant, and  $F(R_{\infty})$  is the K-M function, where  $R_{\infty}$  is the absolute reflectance. The energy band gap of the CdTe sample was estimated from the plot of the K-M function  $[F(R_{\infty}) \cdot hv]^2$  versus  $(hv)$  and extrapolating the linear portion of the graph to the energy axis, as shown in Fig. 4.

The estimated values of the optical energy band gap are 1.56 eV and 1.52 eV for the as-prepared and annealed samples, respectively. The sluggish behavior observed in both plots could be associated with the variation in particle size. Furthermore, the estimated value of  $E_g$  for the as-prepared sample was higher than that of the bulk counterpart because of the nanocrystalline particle size. The band gap decreased for the annealed CdTe sample, which could be due to the increase in the particle size and the improvement in the crystallinity of CdTe after annealing. According to previously

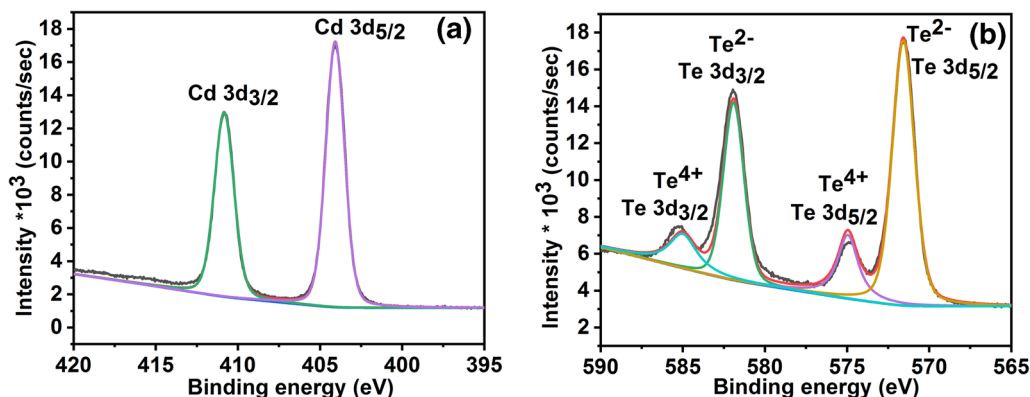
reported works,  $E_g$  decreases with annealing, which may be due to the increased grain size, grain realignment, and decreased internal microstrain.<sup>51,52</sup> In semiconductor materials,  $E_g$  can be influenced by several factors, such as carrier concentration, stoichiometric deviation, recrystallization, quantum confinement effects, dopants, and lattice strain.<sup>53,54</sup>



**Fig. 4** K-M Tauc plots for (a) as-prepared (b) annealed CdTe nanostructure samples.



**Fig. 5** XPS survey spectrum of annealed CdTe nanostructure samples.



**Fig. 6** XPS core level spectra of annealed CdTe nanostructure samples: (a) Cd 3d, (b) Te 3d.

The estimated values of the optical band gap are in close agreement with the standard value of the band gap of CdTe.

## X-ray Photoelectron Spectroscopy

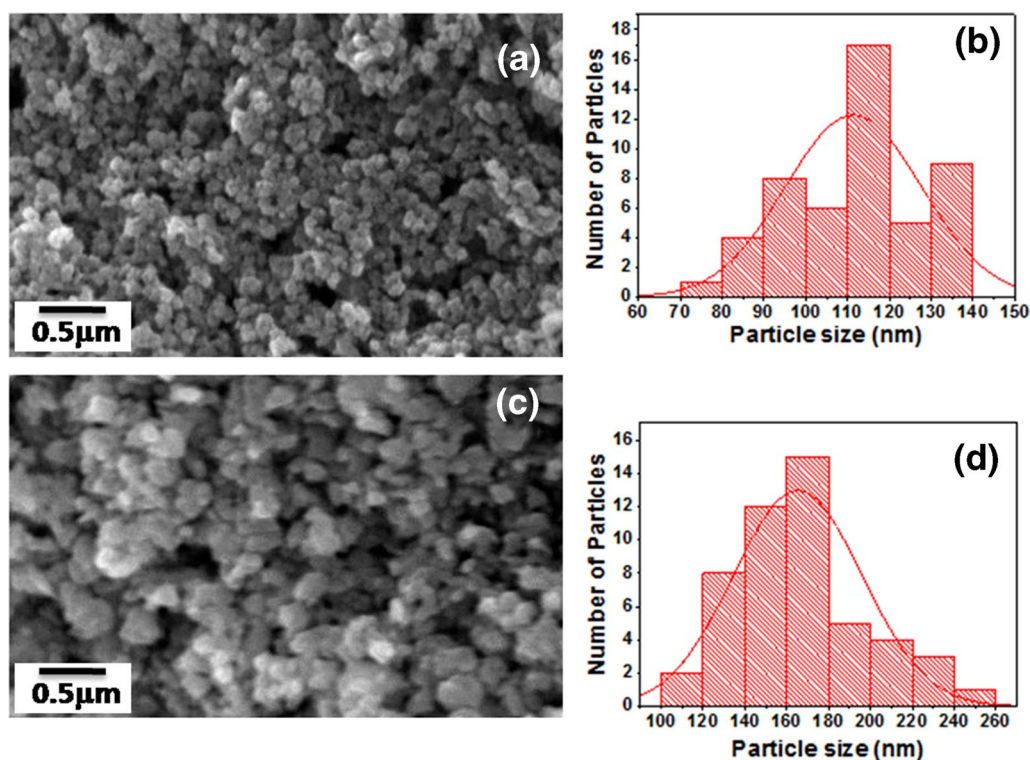
The chemical oxidation states were investigated using XPS analysis. The survey scan shown in Fig. 5 exhibits the presence of Cd, Te, O, and C in the CdTe. The carbon and oxygen peaks in the XPS spectrum may be due to surface contamination by the adsorbed gas molecules. The XPS data were standardized from the carbon peak observed at approximately 282 eV.<sup>55</sup> The core-level high-resolution XPS spectra of Cd and Te in the CdTe material are shown in Fig. 6a and b, respectively.

The Cd 3d peak observed at the binding energies of 404.06 and 410.76 correspond to Cd 3d<sub>5/2</sub> and Cd 3d<sub>3/2</sub>, respectively, for Cd bonded to Te in CdTe. The Te 3d<sub>5/2</sub> and Te 3d<sub>3/2</sub> peaks are divided into two peaks, one of which is connected to Cd-Te bonds and the other to Te-O bonds. The peaks Te 3d<sub>5/2</sub> and Te 3d<sub>3/2</sub> at binding energies of 571.50 eV and 581.96 eV, respectively, represent Te<sup>2-</sup> states in CdTe. Additional peaks observed at 574.86 eV and 585.28 eV corresponding to Te<sup>4+</sup> states within Te-O bonds in TeO<sub>2</sub> can be attributed to oxidation of the CdTe surface. These findings are in good agreement with those of previous studies.<sup>55,56</sup>

## Scanning Electron Microscopy

The SEM images of the as-prepared and annealed CdTe nanostructure samples in Fig. 7 show globular grain morphology. The particles were more regular in the as-prepared CdTe sample and slightly irregular in the annealed CdTe sample. As shown in Fig. 7b and d, ImageJ software was used to calculate the particle size and distribution. Fifty particles were analyzed to determine the length of the individual particles. The average particle sizes of the as-prepared and annealed CdTe samples were 111 nm and





**Fig. 7** (a) SEM image and (b) histogram depicting the distribution of particle sizes for the as-prepared CdTe powder samples. (c) SEM image and (d) histogram depicting the distribution of particle sizes for the annealed CdTe powder samples.

**Table II** Compositional analysis of as-prepared and annealed CdTe powder samples

Sample	Elemental atomic percentage (%) composition	
	Cd	Te
As prepared	48	52
Annealed	51	49

165 nm, respectively, showing that the average particle size increased for the annealed CdTe samples. Previous studies have reported that increasing the temperature promotes grain growth, resulting in larger grain sizes,<sup>7,14</sup> which is primarily attributed to the higher atomic mobility, which enables more atoms to coalesce to form larger grains.<sup>38</sup> In addition, an increased particle size distribution was observed for the annealed CdTe sample. This can be attributed to coalescence and/or recrystallization during the annealing process. The observed morphology demonstrates the potential applications of CdTe thin-film solar cells.

The elemental chemical composition was determined using EDS analysis, and the results are presented in Table II. EDS was performed at three different locations, and the average elemental atomic percentage compositions of Cd and Te were determined for the as-prepared and annealed

CdTe powder samples. The EDS analysis showed that the as-prepared CdTe samples exhibited a slight excess of Te composition. However, during annealing, structural changes occurred in the CdTe sample, leading to a nearly stoichiometric composition. According to the analysis, it can be inferred that annealing significantly affects both the morphology and composition of the CdTe sample.

### Current Density–Voltage Characteristics

The current density–voltage measurements of the as-prepared and annealed CdTe nanostructure samples are shown in Fig. 8. The measurements were performed on circular pellets with a diameter of 5 mm, which were obtained using a hydraulic press under a pressure of 5 t for 10 min at room temperature with no organic binder.

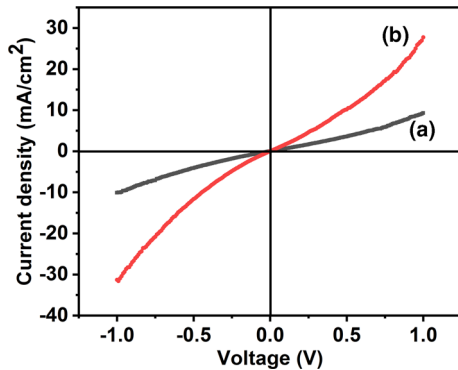
To make metal contact, graphite paste was applied to the pellets. It was observed that the current increases in the annealed CdTe sample, while the as-prepared CdTe sample contained lattice defects. This lattice defects along with the grain boundary area form carrier trap states which confine the free charge carriers by trapping them.<sup>57</sup> After thermal heat treatment, lattice defects and grain boundaries could be reduced due to the increase of crystallinity and crystallite size. As result, there were more available free charge carriers for electrical conduction.<sup>58,59</sup> In addition, charged traps

at grain boundaries create a potential energy barriers. The potential energy barrier hinders movement of free charge carriers across the grain boundaries, while the reduction of grain boundaries decreases the impact of charged traps at the boundaries, improving the current by enhancing mobility.<sup>57</sup> The current density–voltage measurements were used to determine the potential barrier height and ideality factor using<sup>55</sup>:

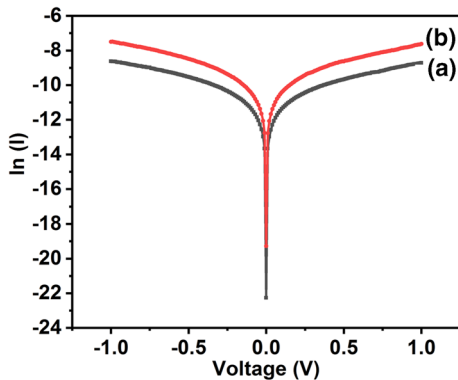
$$\Phi_{b0} = \frac{kT}{q} \ln\left(\frac{AA^*T^2}{I_0}\right) \quad (7)$$

$$\eta = \left(\frac{q}{kT}\right) \left(\frac{dV}{d(\ln I)}\right) \quad (8)$$

where  $A$  is the contact area,  $\eta$  is the ideality factor,  $\Phi_{b0}$  is the potential barrier height, and  $V$  is the applied voltage. The values of  $\eta$  and  $\Phi_{b0}$  were determined from the slope and intercept of the linear region of the  $\ln(I)$  versus voltage ( $V$ ) plot shown in Fig. 9. The calculated values of  $\Phi_{b0}$ ,  $\eta$  for the as-prepared and annealed CdTe samples are 0.59 eV, 3.22 eV, and 0.56 eV, 4.31, respectively.



**Fig. 8** Current–density versus voltage measurements of the (a) as-prepared and (b) annealed CdTe samples.



**Fig. 9**  $\ln(I)$  versus voltage plot of (a) as-prepared and (b) annealed CdTe samples.

An ideality factor  $\eta > 1$  represents a departure from the ideal diode. Conduction mechanisms, generation–recombination currents, tunneling, and electrostatic image forces can cause ideality factors greater than one.<sup>60,61</sup> A decrease in the grain boundaries and the defects at the grain boundaries were observed in the annealed CdTe sample, which leads to an affect on the concentration of free charge carriers at the interface, lowering  $\Phi_{b0}$  and correspondingly increasing the ideality factor ( $\eta$ ).<sup>58,62,63</sup> According to some researchers, the number of charge carriers increases with the annealing temperature. The immobile charge on the semiconductor portion near the interface was neutralized by the free-charge carriers, which is the reason for the reduction in  $\Phi_{b0}$ , and, consequently, the increase in  $\eta$ .<sup>61,64</sup> A decrease in the barrier height may be supported by the corresponding increase in the current.

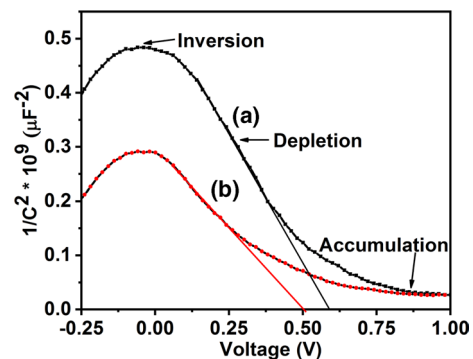
### Capacitance–Voltage Measurements

The capacitance–voltage was measured at a frequency of 100 KHz and the corresponding Mott–Schottky (M-S) ( $1/C^2$  vs.  $V$ ) plots of the as-prepared and annealed CdTe pellets are shown in Fig. 10.

The M-S plot shows three distinct regions: accumulation, depletion, and inversion. The capacitance of a graphite/CdTe pellet/graphite device can be expressed as<sup>65</sup>:

$$\frac{1}{C^2} = \frac{2(V_b + V - kT/q)}{q\epsilon_0\epsilon_s A^2 N_A} \quad (9)$$

where  $V_b$  is the flat-band potential, which can be determined from the extrapolation of the M-S ( $C^{-2}$  vs.  $V$ ) plot to the  $x$ -axis,  $V$  is the applied voltage,  $A$  is the contact area,  $\epsilon_s$  is the static dielectric constant of CdTe ( $\sim 10.2$ ), and  $\epsilon_0$  is the permittivity of free space. The net carrier concentration,  $N_A$ , was determined from the slope of the  $C^{-2}$  versus  $V$  plot and can be obtained from<sup>39</sup>:



**Fig. 10** Capacitance versus voltage measurements of (a) as-prepared and (b) annealed CdTe samples.

**Table III** Electrical parameters of a graphite/CdTe/graphite device calculated from capacitance–voltage and current density–voltage measurements

Sample	Ideality factor	Barrier height (eV)	Carrier concentration $\times 10^{13}/\text{cm}^3$	Flat-band potential (V)
As-prepared	3.22	0.59	4.78	0.58
Annealed	4.31	0.56	7.42	0.50

$$N_A = \frac{2}{q\epsilon_s\epsilon_0 A^2} \left[ \frac{1}{d(C^{-2})/dV} \right] \quad (10)$$

An increase in  $N_A$  and a decrease in  $V_b$  were observed for the annealed CdTe sample. The flat-band potential and carrier concentration values are listed in Table III. As shown in Fig. 10, the junction capacitance of the annealed CdTe sample increased, which could be due to an increase in carrier concentration that results in a reduction in the depletion region and, consequently, an increase in capacitance value.<sup>63,66</sup> The capacitance–voltage results agree with the current density–voltage measurements.

## Conclusions

A simple hydrothermal method was used to produce CdTe nanostructure powder samples. The synthesis process involves the use of cadmium acetate, tellurium dioxide, and  $\text{NaBH}_4$  as reducing agents in double-distilled water as the solvent. The results showed nearly stoichiometric growth of the CdTe nanostructure samples. Annealing the CdTe sample can substantially improve the crystallinity, crystallite size, carrier concentration, and electrical conductance. The physical properties of the CdTe nanostructure samples exhibited a significant dependence on annealing. The results of this study suggest that the obtained CdTe nanostructure samples exhibit favorable physical properties that make them suitable for cost-effective application in CdS/CdTe solar cells.

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**Data Availability** All data generated or analyzed during this study are included in this manuscript.

**Conflict of interest** The authors declare that they have no conflict of interest.

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